## **Msbte News**

## Microprocessor Mcq Questions and Answers Pdf

1) The Microprocessor is aof the
computer and it performs all the computational
tasks.
A) Main
B) Heart Answer
C) Important
D) Simple
2) The Purpose of the Microprocessor is to
control
A) Memory Answer
B) Switching
C) Processing D) Task
D) Task
3) The Intel 8086 Microprocessor is a
processor.
A) 8 bit
B) 16 bit
C) 32 bit Answer
D) 4 bit

4)	The work of EU is
A)	Encoding
B)	Decoding Answer
C)	Processing
D)	Calculations
5)	1MB of memory can be divided into
seg	gment.
A)	1 Kbyte
B)	64 Kbyte Answer
C)	33 Kbyte
D)	34 Kbyte
6)	If MN / MX is low the 8086 operates in
	mode.
A)	Minimum
B)	Maximum Answer
C)	Both a) and b)
D)	Medium
	BIENEW
7)	Which is not an operand ?
A)	Variable
B)	Register
C)	Memory location
D)	Assembler Answer
8)	The number of addresses and data lines of
808	36
A)	8 and 8
B)	16 and 16

- C) 20 and 16 Answer
  D) 16 and 20
  9) The register AX
- 9) The register AX is formed by grouping
- A) AH & AL Answer
- B) BH & BL
- C) CH & CL
- D) DH & DL
- 10) The SS is called as
- A) single stack
- B) stack segment Answer
- C) sequence stack
- D) random stack
- 11) The pin of minimum mode AD0- AD15 has \_\_\_\_\_data bus
- A) 4 bit
- B) 20 bit
- C) 16 bit Answer
- D) 32 bit
- 12) Which is not an operand ?
- A) Variable
- B) Register
- C) Memory location
- D) Assembler Answer

13) The extension that is essential for every assembly level program is A) .ASP B) .ALP C) .ASM Answer D) .PGM
14) The coded object modules of the program to be
assembled are present in
A) ASM file
B) .OBJ file Answer
C) .EXE file
D) .OBJECT file
15) IDIV instruction divides numbers.
A) Signed Answer
B) Unsigned
C) Both
D) None of the above
16) The INC destination increments the content of
destination by
A) 1 Answer
B) 2
C) 30
D) 41

17) T	Γhe	instru	ction	that	adds :	immedia	ate		
data/	/con	tents	of the	memo	ry lo	cation	speci	fied	in
an ir	nstr	uction	/regis	ter t	o the	conter	nts of	anot	her
regis	ster	/memor	y loca	tion	is				

- A) SUB
- B) ADD Answer
- C) MUL
- D) DIV
- 18) The instruction that unconditionally transfers the control of execution to the specified address is\_\_\_\_
- A) CALL
- B) JMP Answer
- C) RET
- D) IRET
- 19) Which of the following is not a machine controlled instruction?
- A) HLT
- B) LOCK
- C) CLC Answer
- D) ESC
- 20) The instruction that loads effective address formed by destination operand into the specified source register is
- A) LEA Answer
- B) LDS
- C) LES

D) LAHF
21) register is used as a default counter
in case of string and loop instructions.
A) BX
B) DX
C) CX Answer
D) AX
22) register is used as an implicit operand
or destination operand in case of arithmetic
instructions and Input-Output instructions.
A) AX
B) DX Answer
C) CX
D) BX
23) The instruction that loads the AH register
with the lower byte of the flag register is
A) SAHF
B) AH
C) LAHF Answer
D) PUSHF
24) The instruction that is used for finding out
the codes in case of code conversion problems is
A) XCHG
B) XLAT Answer
C) XLAT
D) JCXZ

- 25) The instruction that pushes the contents of the specified register/memory location on to the stack is
- A) PUSHF
- B) POP
- C) POPF
- D) PUSH Answer
- 26) The beginning of the macro can be represented as
- A) BEGIN
- B) MACRO Answer
- C) START
- D) NONE OF THE ABOVE
- 27) The end of a macro can be represented by the directive.
- A) ENDM Answer
- B) ENDS
- C) END
- D) ENDD
- 28) The instructions that are used to call a subroutine from the main program and return to the main program after execution of called function are
- A) CALL, RET
- B) CALL, JMP Answer
- C) JMP, IRET
- D) JMP, RET

- 29) The instruction that is used as prefix to an instruction to execute it repeatedly until the CX register becomes zero is
- A) SCAS
- B) REP Answer
- C) CMPS
- D) STOS
- 30) The mnemonic that is placed before the arithmetic operation is performed is
- A) AAA
- B) AAS
- C) AAM
- D) AAD Answer

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